

In application of: Alon et al.

Date: January 23, 2004

Serial No.: 10/091,934

Group Art Unit: 2825

Filed: March 06, 2002

Examiner: Naum B Levin

Confirmation No.: 7058

Date: January 23, 2004

For: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED CIRCUIT DESIGN

Commissioner for Patents
Washington, D. C. 20231

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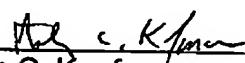
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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ASSISTANT COMMISSIONER FOR PATENTS
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Sir:

Transmitted herewith is an Amendment in the above-identified application.

No additional fee is required.
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INDEP. 9	MINUS	*** 9
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Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

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